

CERN OHL

**F4GRX**

Sheet: /

File: ap.sch

**Title: Radio gateway (overview)**

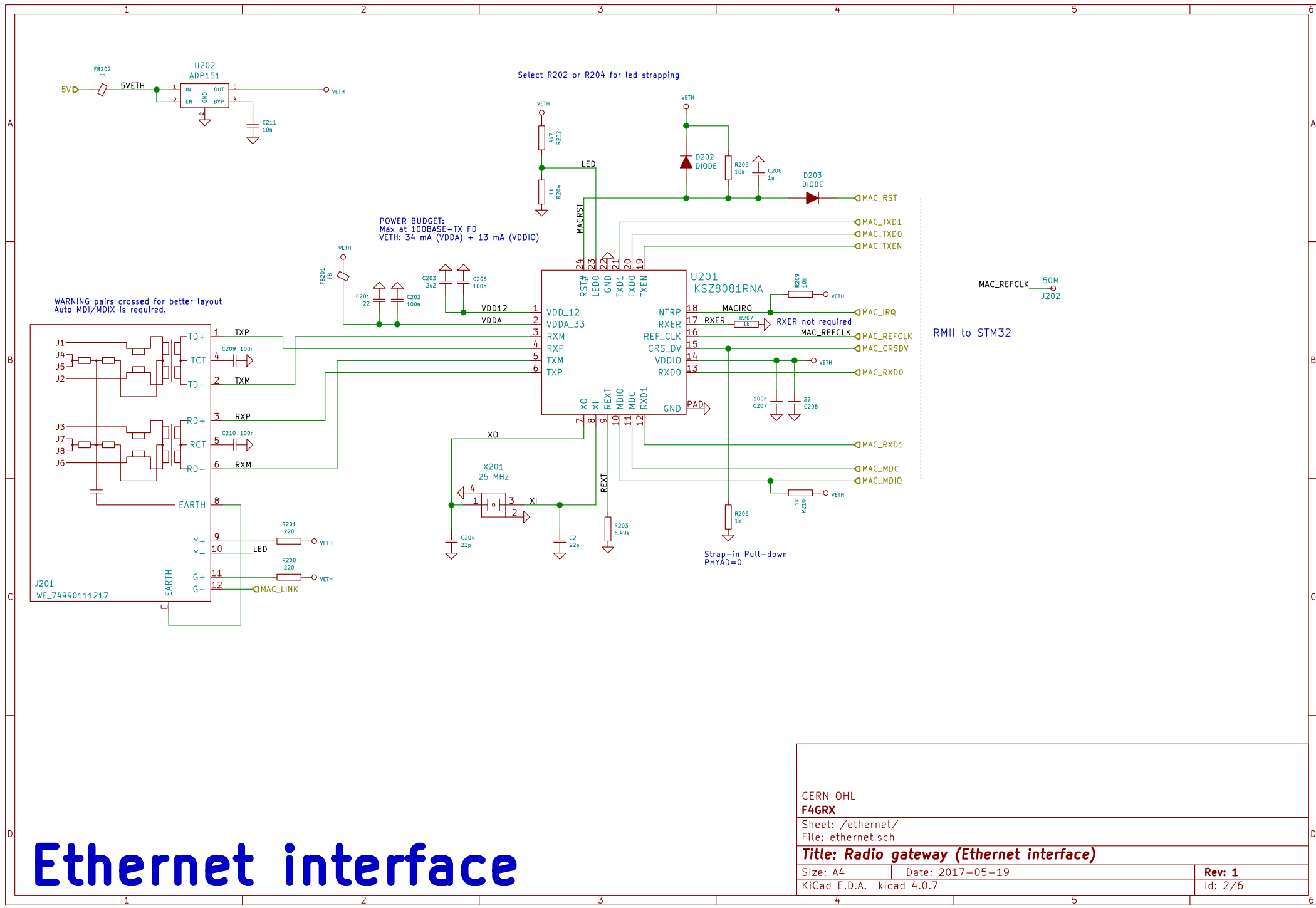
Size: A4

Date: 2017-05-19

**Rev: 1**

KiCad E.D.A. kicad 4.0.7

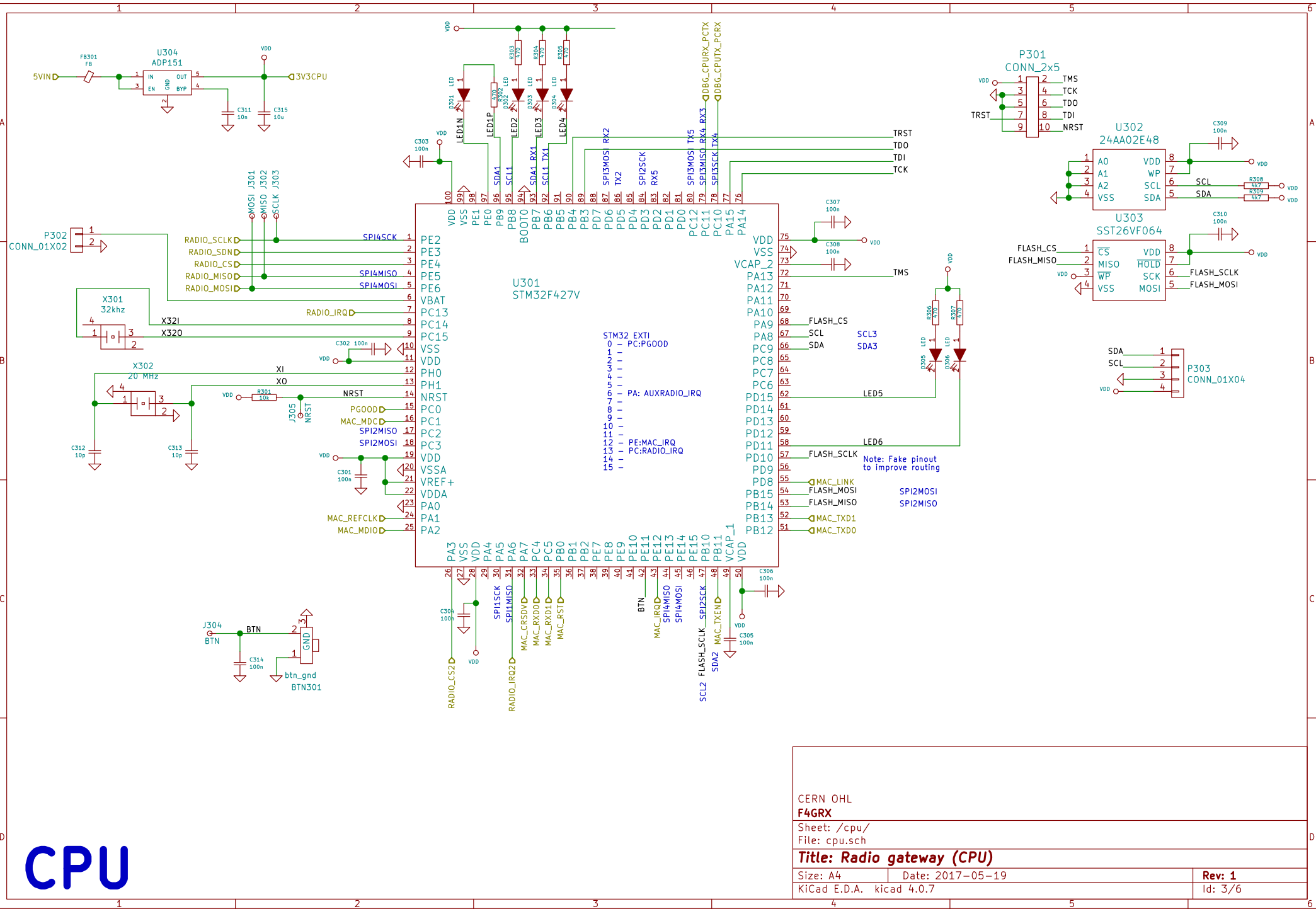
Id: 1/6



# Ethernet interface

CERN OHL	
F4GRX	
Sheet: /ethernet/	
File: ethernet.sch	
Title: Radio gateway (Ethernet interface)	
Size: A4	Date: 2017-05-19
KiCad E.D.A. kicad 4.0.7	Rev: 1
	Id: 2/6

# CPU



CERN OHL  
F4GRX

Sheet: /cpu/  
File: cpu.sch

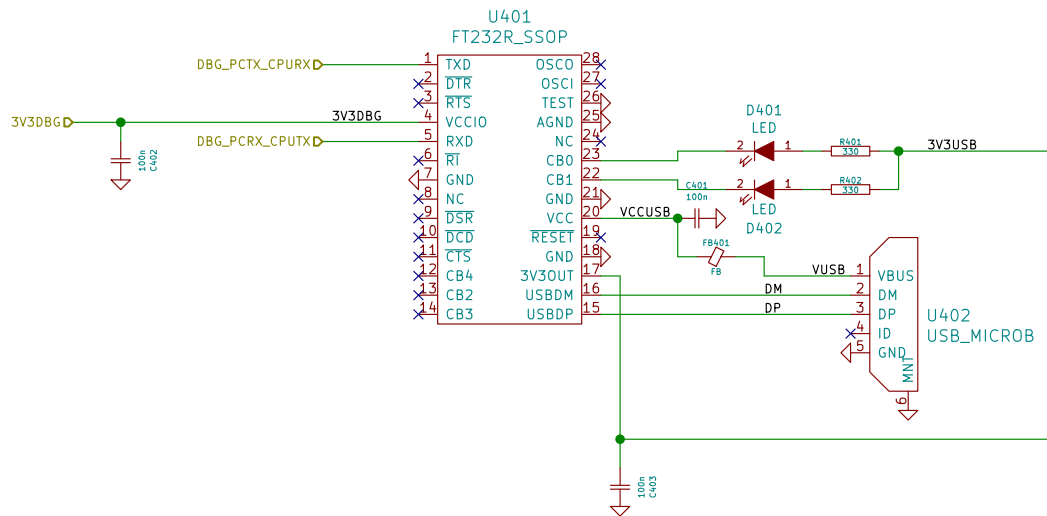
**Title: Radio gateway (CPU)**

Size: A4 Date: 2017-05-19

KiCad E.D.A. kicad 4.0.7

Rev: 1  
Id: 3/6

DEBUG



CERN OHL

**F4GRX**

Sheet: /debug/

File: debug.sch

**Title: Radio gateway (Debug UART)**

Size: A4 Date: 2017-05-19

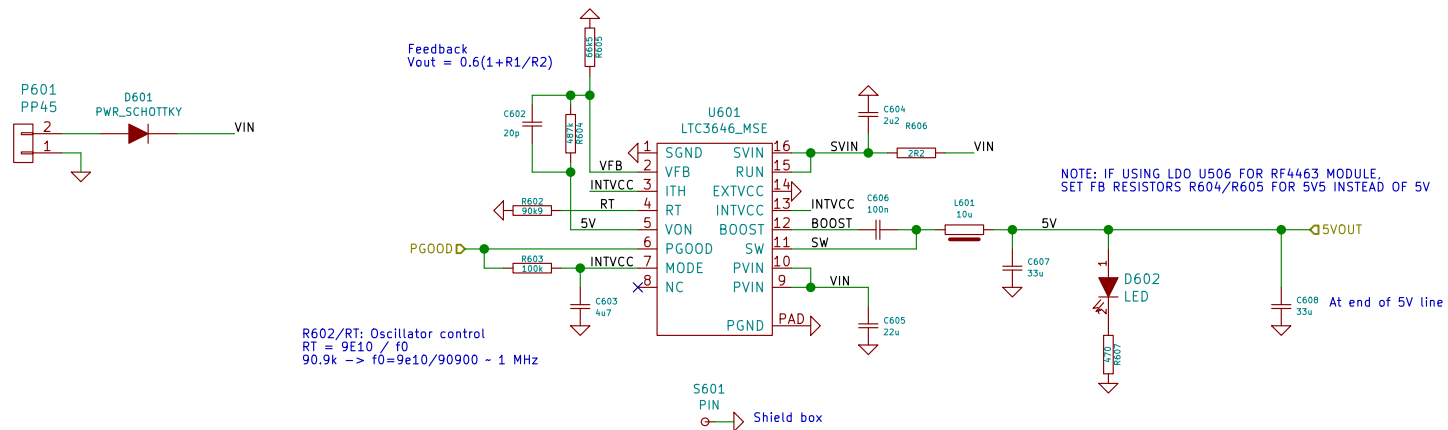
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**Rev: 1**

Id: 4/6



# Power



CERN OHL  
**F4GRX**

Sheet: /power/  
File: power.sch

**Title: Radio gateway (Power supply)**

Size: A4 Date: 2017-07-05

KiCad E.D.A. kicad 4.0.7

Rev: 1

Id: 6/6