

CERN OHL

**F4GRX**

Sheet: /

File: ap.sch

**Title: Radio gateway (overview)**

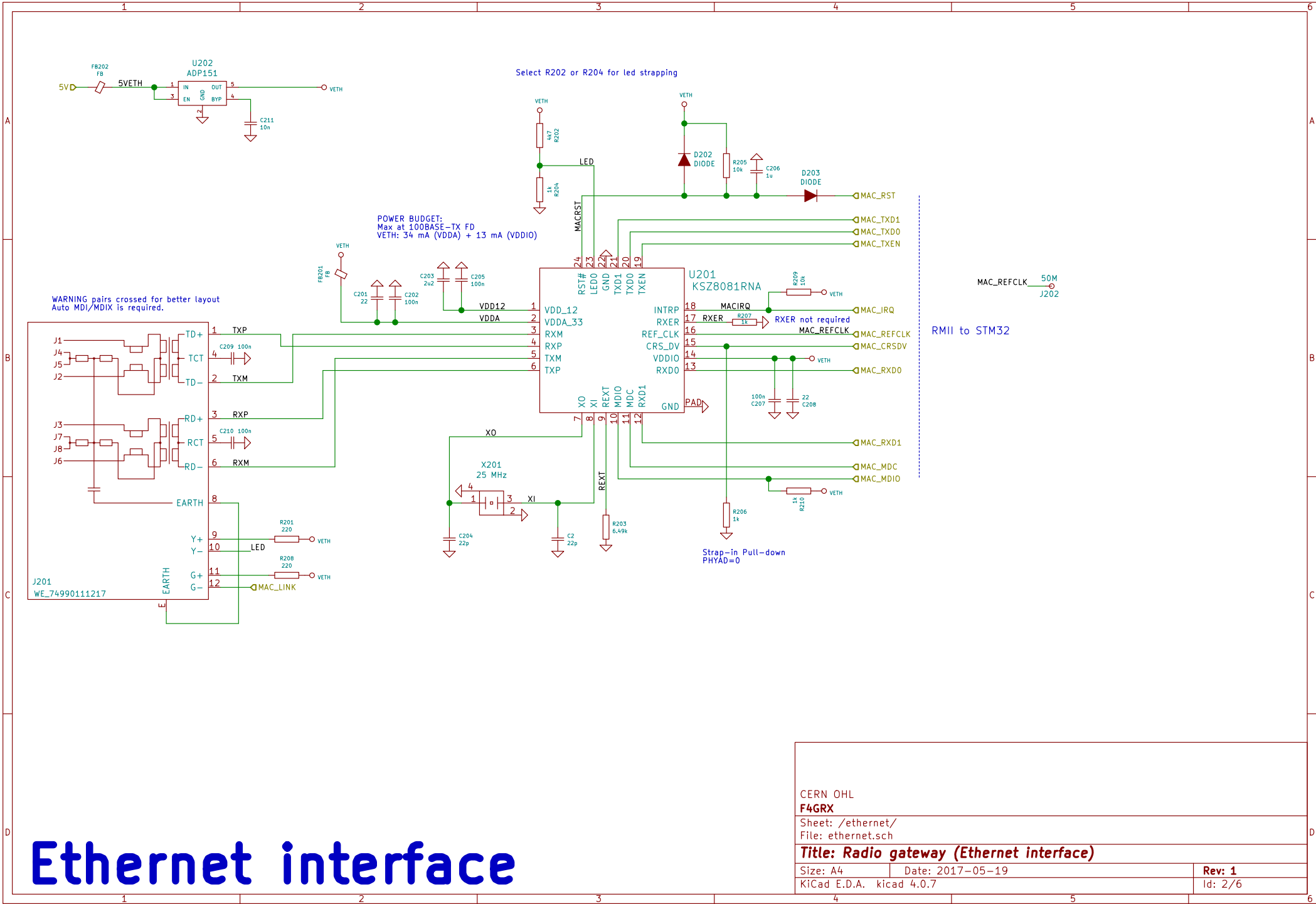
Size: A4

Date: 2017-05-19

**Rev: 1**

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Id: 1/6



Select R202 or R204 for led strapping

POWER BUDGET:  
Max at 100BASE-TX FD  
VETH: 34 mA (VDDA) + 13 mA (VDDIO)

WARNING pairs crossed for better layout  
Auto MDI/MDIX is required.

RMII to STM32

Strap-in Pull-down  
PHYAD=0

# Ethernet interface

CERN OHL  
**F4GRX**

Sheet: /ethernet/  
File: ethernet.sch

**Title: Radio gateway (Ethernet interface)**

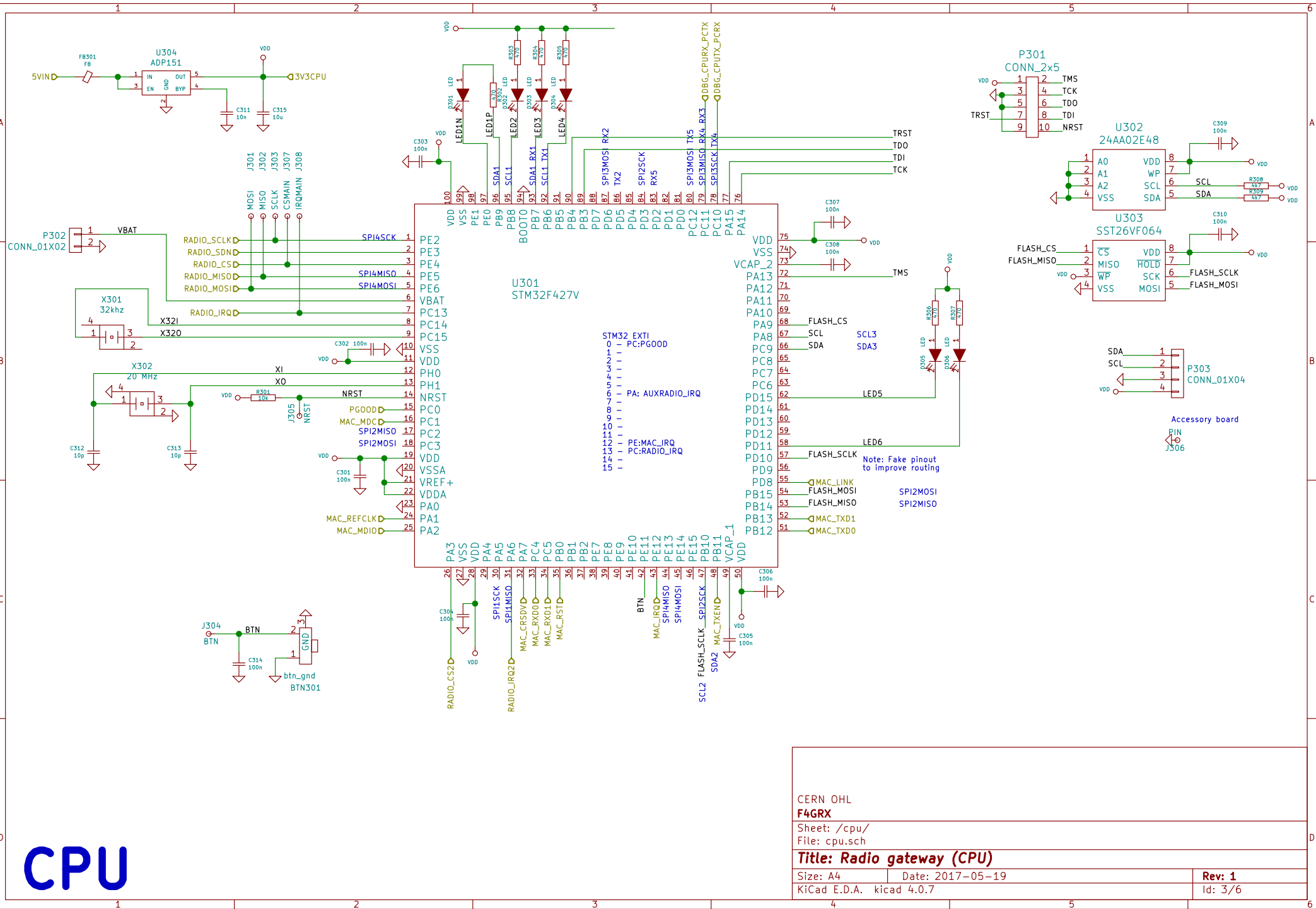
Size: A4 Date: 2017-05-19

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Rev: 1

Id: 2/6

# CPU



CERN OHL  
F4GRX

Sheet: /cpu/  
File: cpu.sch

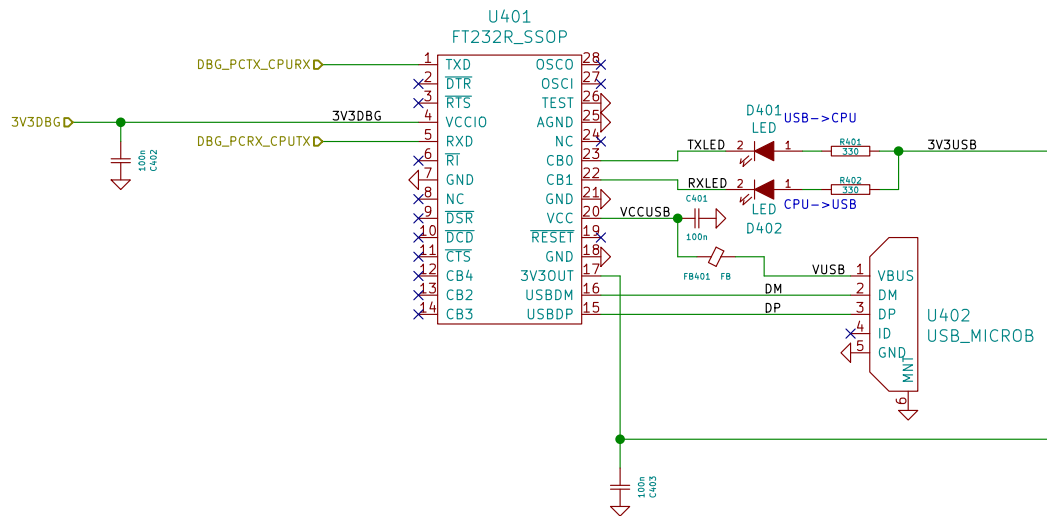
**Title: Radio gateway (CPU)**

Size: A4 Date: 2017-05-19

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Rev: 1  
Id: 3/6

DEBUG



CERN OHL

**F4GRX**

Sheet: /debug/

File: debug.sch

**Title: Radio gateway (Debug UART)**

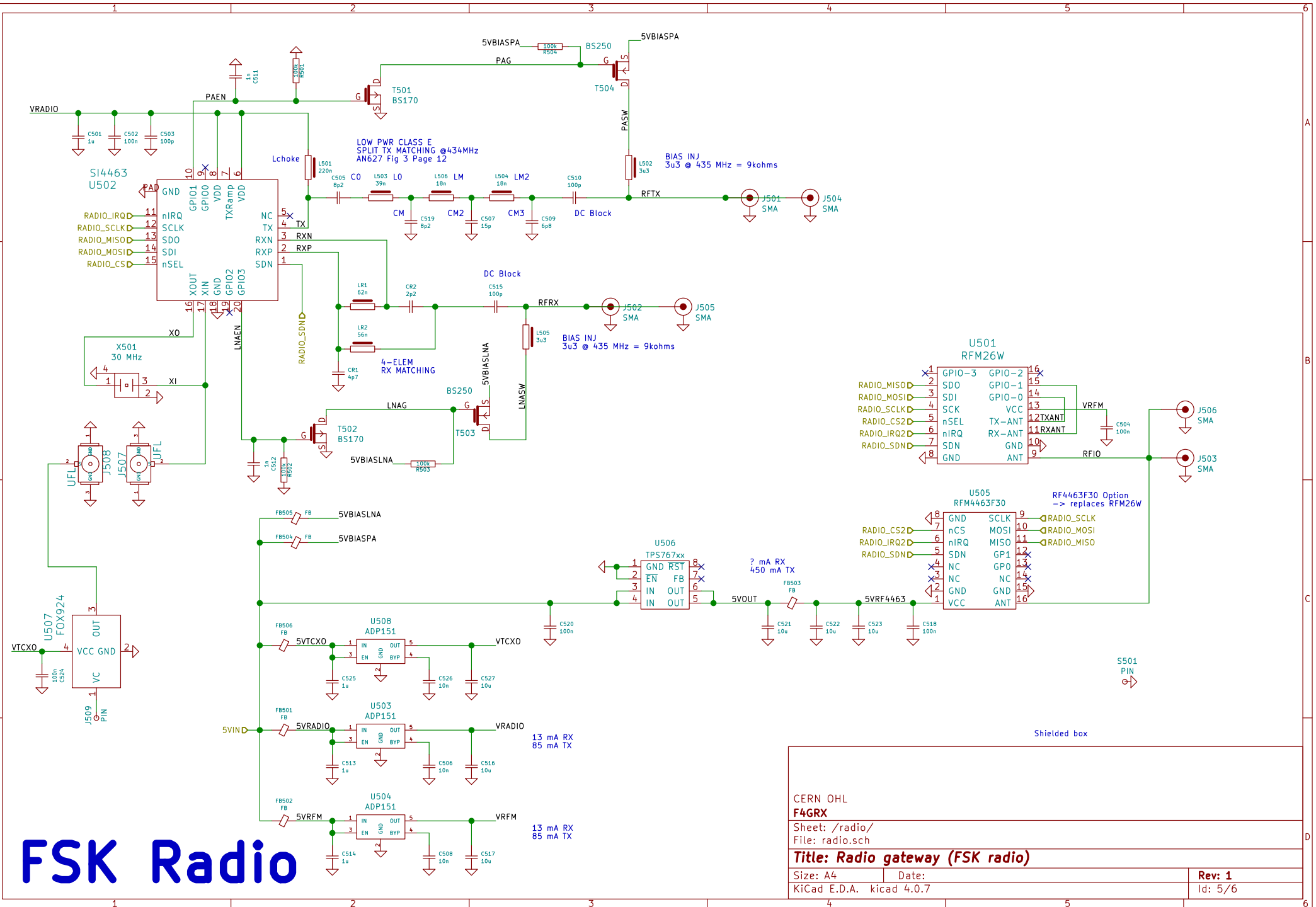
Size: A4 Date: 2017-05-19

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**Rev: 1**

Id: 4/6

# FSK Radio



CERN OHL  
**F4GRX**

Sheet: /radio/  
File: radio.sch

**Title: Radio gateway (FSK radio)**

Size: A4  
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Date:  
Rev: 1  
Id: 5/6



At end of 5V line

Shield box

Rev: 1  
Id: 6/6